



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
MEARS

Serial No. NOT YET ASSIGNED

Filing Date: HEREWITH

For: METHOD FOR MAKING
SEMICONDUCTOR DEVICE
INCLUDING BAND-ENGINEERED
SUPERLATTICE

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SIGNATURE: Justin Goree

CITATION UNDER 37 CFR §1.97

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference may be found in parent application, Serial No. 10/647,061 filed August 22, 2003. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

Respectfully submitted,

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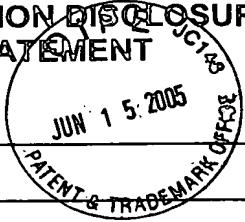
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|-----------------------------------------------------------------------------------|--|---------------------------------------------------------------------|-----------------------------------------------------|
| INFORMATION DISCLOSURE STATEMENT | | Atty Docket: Serial No.: Applicant: Filing Date: Group: | 62603_CON2 NOT YET ASSIGNED MEARS HEREWITH |
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| Examiner Initials | | Document Number | Date | Name | Class | Sub Class | Filing Date |
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| INFORMATION DISCLOSURE STATEMENT | Atty Docket: Serial No.: Applicant: Filing Date: Group: | 62603_CON2 NOT YET ASSIGNED MEARS HEREWITH |
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